

REMARKS

Claims 1-5 and 7-14 remain pending in the application. Claims 9, 12 and 14 are amended. Reconsideration of the rejection and allowance of the pending application in view of the following remarks are respectfully requested.

In the Office Action, the Examiner rejected claims 1-14 under 35 U.S.C. §102(b) as being anticipated by Kahn et al. (U.S. Patent No. 6,525,775). Based upon the Office Action, it appears that the Examiner has overlooked the Article 19(1) amendments to the claims, which were filed in the international stage of the present PCT application. Applicant wishes to bring to the attention of the Examiner that a copy of the Article 19(1) amendment was filed in the present national stage application on September 23, 2005.

Applicant's claim 1 recites a data reproduction apparatus capable of being connected via an interface bus to a video display apparatus, which includes, inter alia, a transmitter that transmits video data and audio data using a first area and a second area, respectively, and a controller that controls the transmitter to transmit update software to the video display apparatus using a third area. The interface bus includes a data line and a clock line that transmits a clock signal. The transmitter outputs a clock signal to the clock line while transmitting, in synchronization with the clock signal, the update software via the data line using the third area.

Applicant's claim 9 recites a video display apparatus capable of being connected to a data reproduction apparatus via an interface bus. The interface bus includes a data line that transmits video data, audio data, and additional data using a first area, a second area, and a third area, respectively, and a clock line that transmits a clock signal. The video display apparatus includes, inter alia, a receiver that receives update software

transmitted in synchronization with the clock signal output to the clock line by the data reproduction apparatus, using the third area.

Applicant's claim 12 recites a software updating system which includes, inter alia, a video display apparatus, and a data reproduction apparatus capable of being connected to the video display apparatus via an interface bus. The data reproduction apparatus includes a transmitter that transmits video data and audio data using a first area and a second area, respectively, and a controller that controls the transmitter to transmit update software to the video display apparatus via the interface bus, using a third area. The interface bus includes a data line and a clock line. The transmitter outputs a clock signal to the clock line while transmitting, in synchronization with the clock signal, the update software via the data line using the third area.

Applicant's claim 14 recites a software updating method for updating software for a video display apparatus using a data reproduction apparatus capable of being connected to the video display apparatus via an interface bus. The method includes, inter alia, transmitting update software to the video display apparatus via the interface bus, using a third area for transmission of additional data other than a first area for transmission of video data and a second area for transmission of audio data. The interface bus includes a data line, and a clock line that transmits a clock signal. Transmitting the update software includes outputting the clock signal to the clock line while transmitting, in synchronization with the clock signal, the update software via the data line using the third area.

Kahn et al. discloses a digital VCR 113 which is coupled to a set-top box (STB) 90 via a digital communications channel 96, and provides digital audio, video and data

over the channel 96. A data program provided by the digital VCR 113 includes programming information which reconfigures the STB 90. See, e.g., Fig. 1A and col. 3, lines 12-25 of Kahn et al.

The STB 90 includes a video decoder section 120, a Decoder Memory 130, and an audio decoder section 160. See, e.g., Fig. 1B and col. 3, lines 63-67 of Kahn et al. The video decoder section 120 includes a digital phase locked loop (DPLL) 122, which generates timing signals to synchronize processing operations between the video decoder section 120, the Decoder Memory 130, and the audio decoder section 160. See, e.g., Fig. 1B and col. 4, lines 36-37 and 50-53 of Kahn et al.

Applicant respectfully submits that Kahn et al. fails to disclose or suggest that the digital VCR 113 outputs a clock signal to the communications channel 96 and transmits the programming information to the STB 90 in synchronization with the clock signal. On page 5 of the Office Action, in addressing original claim 6, the Examiner cites col. 4, lines 50-55 of Kahn et al. Applicant respectfully submit that this citation merely discloses that the DPLL 122 generates timing signals to synchronize processing operations within the STB 90 (i.e., between the video decoder section 120, the Decoder Memory 130, and the audio decoder section 160), and fails to suggest that a clock signal is transmitted from the digital VCR 113 to the STB 90 for synchronizing transmission of the programming information.

Accordingly, Applicant respectfully submits that Kahn et al. does not disclose: a transmitter that outputs a clock signal to a clock line while transmitting, in synchronization with the clock signal, update software via a data line, as recited in claims 1 and 12; a receiver that receives update software transmitted in synchronization with a

clock signal output to a clock line by a data reproduction apparatus, as recited in claim 9; or a method which includes outputting a clock signal to a clock line while transmitting, in synchronization with a clock signal, update software via a data line, as recited in claim 14.

For at least these reasons, Applicant respectfully submits that the inventions recited in Applicant's independent claims 1, 9, 12 and 14 are not anticipated by Kahn et al., and thus, requests that the Examiner withdraw the rejection under 35 U.S.C. §102(b) and allow these claims.

Applicant submits that claims 2-5, 7, 8, 10, 11 and 13 are also in condition for allowance, in view of their dependency from claims 1, 9 and 12.

Based on the above, it is respectfully submitted that this application is in condition for allowance, and a Notice of Allowance is respectfully requested.

SUMMARY AND CONCLUSION

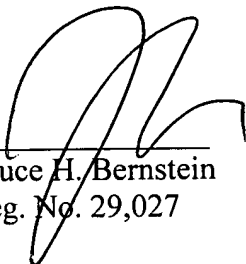
Reconsideration of the outstanding Office Action, and allowance of the present application and all of the claims therein are respectfully requested and believed to be appropriate. Applicant has made a sincere effort to place the present invention in condition for allowance and believes that he has done so.

Any amendments to the claims which have been made in this amendment, and which have not been specifically noted to overcome a rejection based upon the prior art, should be considered to have been made for a purpose unrelated to patentability, and no estoppel should be deemed to attach thereto.

Should an extension of time be necessary to maintain the pendency of this application, including any extensions of time required to place the application in condition for allowance by an Examiner's Amendment, the Commissioner is hereby authorized to charge any additional fee to Deposit Account No. 19-0089.

Should the Examiner have any questions or comments regarding this response, or the present application, the Examiner is invited to contact the undersigned at the below-listed telephone number.

Respectfully Submitted,
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